

ST AVAILABLE  
BEST AVAILABLE COPY

## SEARCHED

Class	Sub.	Date	Exmr.
703	23	4-16-03	DMC

## SEARCH NOTES (INCLUDING SEARCH STRATEGY)

Search Notes	Date	Exmr.
emulation		
logic cell		
FPGA		
emulation engine		
generation logic		
time - multiplexing		
hybrid EP64		
IEEE		
wave logic		
signal		
function cell		
ACM	4-19-03	DMC
logic simulation		
time multiplexed		
IEEE	4-24-04	DMC
wave logic		
wave signal		
ACM		
wave logic		
IEEE		
emulator program		
cells		
ACM		
logic connection		
Signal enable		
array cells		
emulation		
EAST	4-24-04	DMC
model clock		
emulator		
logic cell		
processor		
test bench		
FPGA configuration		
Hugh Jones	4-24-04	DMC
P. Examine 2128		

## INTERFERENCE SEARCHED

Class	Sub.	Date	Exmr.